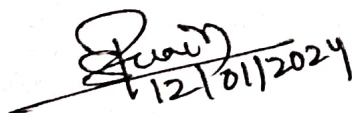


Lesson Plan for the Session Summer-2024
(4th SEMESTER CSE)

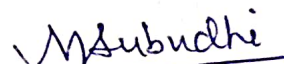
DISCIPLINE: Computer Science & Engineering	SEMESTER: 4TH	NAME OF THE TEACHING FACULTY: Mr. Pramod Kumar Swain
SUBJECT: Operating System	NO. OF DAYS/PER WEEK CLASS ALLOTTED: 4	SEMESTER DURATION: 16/01/2024 to 26/4/2024 NO.OF WEEKS : 15
WEEK	CLASSDAY	THEORY/PRACTICALTOPICS
1 ST	1 ST	Objectives and Explain functions of operating system.
	2 ND	Evolution of Operating system
	3 RD	Structure of operating system
	4 TH	PROCESSMANAGEMENT Process concept
2 ND	1 ST	process control, interacting processes
	2 ND	Inter process messages.
	3 RD	Implementation issues of Processes.
	4 TH	Process scheduling
3 RD	1 ST	Job scheduling
	2 ND	Process synchronization
	3 RD	semaphore
	4 TH	Principle of concurrency
4 TH	1 ST	types of scheduling
	2 ND	MEMORY MANAGEMENT Memory allocation Techniques
	3 RD	Contiguous memory allocation
	4 TH	non contiguous memory allocation
5 TH	1 ST	Swapping
	2 ND	Paging
	3 RD	Segmentation,
	4 TH	virtual memory using paging,
6 TH	1 ST	Demand paging
	2 ND	page fault handling
	3 RD	DEVICEMANAGEMENT
	4 TH	Techniques for Device Management
7 TH	1 ST	Dedicated
	2 ND	shared ,virtual
	3 RD	Device allocation considerations I/O traffic control
	4 TH	I/O Schedule
8 TH	1 ST	I/O Device handlers.
	2 ND	SPOOLING
	3 RD	DEADLOCKS

	4TH	Concept of deadlock
9TH	1ST	Types of deadlock
	2ND	Dead Lock Detection
	3RD	Resources allocation Graph
	4TH	Methods of Deadlock handling
10TH	1ST	Recovery & Prevention
	2ND	Explain Bankers Algorithm
	3RD	Safety Algorithm
	4TH	FILEMANAGEMENT
11TH	1ST	File organization
	2ND	Directory & file structure
	3RD	sharing of files
	4TH	File access methods
12TH	1ST	file systems
	2ND	reliability
	3RD	Allocation of disk space
	4TH	File protection
13TH	1ST	secondary storage management
	2ND	SYSTEMPROGRAMMING
	3RD	Concept of system programming
	4TH	and show difference from Application Compiler
14TH	1ST	Compiler
	2ND	Concept of compiler
	3RD	functions of compiler
	4TH	functions of interpreter
15TH	1ST	Compare compiler and interpreter
	2ND	Seven phases of compiler
	3RD	Seven phases of compiler
	4TH	brief description of each phase

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DISCIPLINE: Computer Science & Engineering	SEMESTER: 4TH	NAME OF THE TEACHING FACULTY: Mrs. Yogeswari Magar
SUBJECT: Data Communication and Computer Network	NO. OF DAYS/PER WEEK CLASS ALLOTTED: 4	SEMESTER DURATION: 16/01/2024 to 26/4/2024 NO.OF WEEKS : 15
WEEK	CLASSDAY	THEORY/PRACTICALTOPICS
1 ST	1 ST	Network
	2 ND	Protocol
	3 RD	Data Communication
	4 TH	Architecture
2 ND	1 ST	Standards
	2 ND	OSI
	3 RD	TCP/IP
	4 TH	Data Transmission &Media
3 RD	1 ST	Data transmission Concepts and Terminology
	2 ND	Analog and Digital Data transmission
	3 RD	Transmission impairments
	4 TH	Channel capacity
4 TH	1 ST	Transmission media
	2 ND	Guided Transmission
	3 RD	Wireless Transmission
	4 TH	Data Encoding
5 TH	1 ST	Concept of Data Encoding
	2 ND	Digital data digital signals
	3 RD	Digital data analog signals
	4 TH	Analog data digital signals
6 TH	1 ST	Analog data analog signals
	2 ND	Data Communication & Data link control
	3 RD	Asynchronous and Synchronous Transmission
	4 TH	Error Detection
7 TH	1 ST	Line configuration
	2 ND	Flow Control
	3 RD	Error Control
	4 TH	Multiplexing
8 TH	1 ST	FDM synchronous TDM
	2 ND	Statistical TDM
	3 RD	Switching &Routing
	4 TH	Circuit Switching networks
9 TH	1 ST	Packet Switching principles

	2 ND	X.25
	3 RD	Routing in Packet switching
	4 TH	Congestion
10 TH	1 ST	Effects of congestion
	2 ND	Congestion control
	3 RD	Congestion control
	4 TH	Traffic Management
11 TH	1 ST	Congestion Control in Packet Switching Network
	2 ND	LAN Technology
	3 RD	Topology
	4 TH	Transmission Media
12 TH	1 ST	LAN protocol architecture
	2 ND	Medium Access control
	3 RD	Medium Access control
	4 TH	Bridges, Hub, Switch
13 TH	1 ST	Ethernet (CSMA/CD),
	2 ND	Ethernet (CSMA/CD),
	3 RD	CSMA/CA
	4 TH	Fiber Channel
14 TH	1 ST	Wireless LAN Technology
	2 ND	TCP/IP
	3 RD	TCP/IP Protocol Suite
	4 TH	Basic Protocol functions
15 TH	1 ST	Principles of Inter networking
	2 ND	Principles of Inter networking
	3 RD	Internet Protocol operations
	4 TH	Internet Protocol

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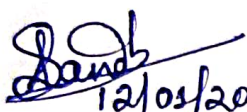
M. Subudhi
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DISCIPLINE: Computer Science & Engineering	SEMESTER: 4TH	NAME OF THE TEACHING FACULTY: Mrs. Lipika Sandha
SUBJECT: MICROPROCESSOR & MICROCONTROLLER	NO. OF DAYS/PER WEEK CLASS ALLOTTED: 5	SEMESTER DURATION: 16/01/2024 to 26/4/2024 NO.OF WEEKS : 15
WEEK	CLASSDAY	THEORY/PRACTICALTOPICS
1 ST	1 ST	Microprocessor
	2 ND	Introduction to Microprocessor and Microcomputer & distinguish between them.
	3 RD	Concept of Address bus, data bus, control bus & System Bus
	4 TH	General Bus structure Block diagram.
2 ND	1 ST	Basic Architecture of 8085 (8 bit)Microprocessor
	2 ND	Basic Architecture of 8085 (8 bit)Microprocessor
	3 RD	Signal Description (Pin diagram) of 8085Microprocessor
	4 TH	Signal Description (Pin diagram) of 8085Microprocessor
3 RD	1 ST	Register Organizations, Distinguish between SPR & GPR, Timing & Control Module,)
	2 ND	Interrupts:-8085 Interrupts, Masking of Interrupt(SIM,RIM
	3 RD	Interrupts:-8085 Interrupts, Masking of Interrupt(SIM,RIM
	4 TH	Addressing data & Differentiate between one-byte, two-byte &three-byte instructions with examples
4 TH	1 ST	Addressing data & Differentiate between one-byte, two-byte &three-byte instructions with examples
	2 ND	Addressing data & Differentiate between one-byte, two-byte &three-byte instructions with examples
	3 RD	Addressing modes in instructions with suitable examples
	4 TH	Addressing modes in instructions with suitable examples
5 TH	1 ST	Instruction Set of 8085(Data Transfer, Arithmetic, Logical, Branching, Stack& I/O , Machine Control)
	2 ND	Instruction Set of 8085(Data Transfer, Arithmetic, Logical, Branching, Stack& I/O , Machine Control)
	3 RD	Simple Assembly Language Programming of8085 Simple Addition &Subtraction
	4 TH	Logic Operations (AND, OR, Complement 1's & 2's) & Masking of
6 TH	1 ST	Counters & Time delay (Single Register, Register Pair, More than Two Register)
	2 ND	Looping, Counting & Indexing (Call/JMP etc).
	3 RD	Stack &Subroutines programs, Code conversion, BCD Arithmetic & 16 Bit data Operation, Block Transfer.
7 TH	1 ST	Memory & I/O Addressing
	2 ND	TIMINGDIAGRAMS

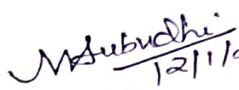
	3 RD	Define op code, operand, T-State, Fetch cycle, Machine Cycle, Instruction cycle & discuss the concept of timing diagram.
	4 TH	Draw timing diagram for memory read, memory write, I/O read, I/O write machine cycle.
8 TH	1 ST	Draw a neat sketch for the timing diagram for 8085 instruction (MOV, MVI, LDA instruction).
	2 ND	Draw a neat sketch for the timing diagram for 8085 instruction (MOV, MVI, LDA instruction).
	3 RD	Microprocessor Based System Development Aids
	4 TH	Concept of interfacing
9 TH	1 ST	Define Mapping & Data transfer mechanisms - Memory mapping & I/O Mapping
	2 ND	Concept of Memory Interfacing:- Interfacing EPROM & RAM Memories
	3 RD	Concept of Address decoding for I/O devices
	4 TH	Programmable Peripheral Interface: 8255
10 TH	1 ST	ADC & DAC with Interfacing
	2 ND	Draw a neat sketch for the timing diagram for 8085 instruction (MOV, MVI, LDA instruction).
	3 RD	Interfacing Seven Segment Displays
	4 TH	Generate square waves on all lines of 8255
11 TH	1 ST	Design Interface a traffic light control system using 8255.
	2 ND	Microprocessor (Architecture and Programming-16 bit-8086)
	3 RD	Register Organization of 8086
	4 TH	Internal architecture of 8086
12 TH	1 ST	Signal Description of 8086
	2 ND	General Bus Operation & Physical Memory Organization
	3 RD	Minimum Mode & Timings, Maximum Mode & Timings
	4 TH	Interrupts and Interrupt Service Routines, Interrupt Cycle,
13 TH	1 ST	Non-Maskable Interrupt, Maskable Interrupt
	2 ND	8086 Instruction Set & Programming: Addressing Modes, Instruction Set, Assembler Directives and Operators
	3 RD	8086 Instruction Set & Programming: Addressing Modes, Instruction Set, Assembler Directives and Operators
	4 TH	Simple Assembly language programming using 8086 instructions.
14 TH	1 ST	Simple Assembly language programming using 8086 instructions.
	2 ND	Distinguish between Microprocessor & Microcontroller
	3 RD	8 bit & 16 bit microcontroller
	4 TH	CISC & RISC processor
15 TH	1 ST	Architecture of 8051 Microcontroller
	2 ND	Signal Description of 8051 Microcontrollers
	3 RD	Memory Organization-RAM structure, SFR
	4 TH	Simple 8051 Assembly Language Programming Arithmetic & Logic Instructions, JUMP, LOOP, CALL

Machine
of timing
write.

		Instructions, I/O Port Programming
	2 ND	Simple 8051 Assembly Language Programming Arithmetic & Logic Instructions, JUMP, LOOP, CALL Instructions, I/O Port Programming
	3 RD	Interrupts
	4 TH	Timer & Counters
15 TH	1 ST	Timer & Counters
	2 ND	Serial Communication
	3 RD	Microcontroller Interrupts and Interfacing to 8255
	4 TH	Microcontroller Interrupts and Interfacing to 8255


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Lepika Sarda
Lect. (Electronics)


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DISCIPLINE: Computer Science & Engineering		SEMESTER: 4TH	NAME OF THE TEACHING FACULTY: Mrs. MOUSUMI SUBUDHI
SUBJECT: DATABASE MANAGEMENT SYSTEM		NO. OF DAYS/PER WEEK CLASS ALLOTTED: 4	SEMESTER DURATION: 16/01/2024 to 26/4/2024 NO. OF WEEKS : 15
WEEK	CLASSDAY	THEORY/PRACTICAL TOPICS	
1 ST	1 ST	BASIC CONCEPTS OF DBMS	
	2 ND	Purpose of database Systems	
	3 RD	Explain Data abstraction	
	4 TH	Data base users	
2 ND	1 ST	Data definition language	
	2 ND	Data Dictionary	
	3 RD	Data independence	
	4 TH	Entity relationship models	
3 RD	1 ST	Entity sets and Relationship sets	
	2 ND	Explain Attributes	
	3 RD	Mapping constraints	
	4 TH	E-R Diagram	
4 TH	1 ST	E-R Diagram	
	2 ND	Relational model	
	3 RD	Hierarchical model	
	4 TH	Network model	
5 TH	1 ST	RELATIONAL DATABASE	
	2 ND	Introduction	
	3 RD	Relation algebra	
	4 TH	Different operators select, project, join , simple Examples	
6 TH	1 ST	Different operators select, project, join , simple Examples	
	2 ND	Different operators select, project, join , simple Examples	
	3 RD	Different operators select, project, join , simple Examples	
	4 TH	NORMALIZATION IN RELATIONAL SYSTEM	
7 TH	1 ST	Introduction	
	2 ND	Functional Dependencies	
	3 RD	Functional Dependencies	
	4 TH	Lossless join	
8 TH	1 ST	Importance of normalization	
	2 ND	Compare First second and third normal forms	
	3 RD	Compare First second and third normal forms	
	4 TH	Compare First second and third normal forms	
	1 ST	Explain BCNF	
	2 ND	Explain BCNF	
		STRUCTURED QUERY LANGUAGE	
		Introduction	

	3 RD	Elementary idea of Query language
	4 TH	Queries in SQL
9 TH	1 ST	Queries in SQL
	2 ND	Simple queries to create, update, insert in SQL
	3 RD	Simple queries to create, update, insert in SQL
	4 TH	Simple queries to create, update, insert in SQL
	2 ND	Simple queries to create, update, insert in SQL
	3 RD	TRANSACTION PROCESSING CONCEPTS
	4 TH	Introduction
11 TH	1 ST	Idea about transaction processing
	2 ND	Transaction & system concept
	3 RD	Transaction & system concept
	4 TH	Desirable properties of transaction
12 TH	1 ST	Desirable properties of transaction
	2 ND	Schedules and recoverability
	3 RD	Schedules and recoverability
	4 TH	CONCURRENCY CONTROL CONCEPTS
13 TH	1 ST	Basic concepts
	2 ND	Locks, Live Lock, Dead Lock,
	3 RD	Locks, Live Lock, Dead Lock,
	4 TH	Locks, Live Lock, Dead Lock,
14 TH	1 ST	Locks, Live Lock, Dead Lock,
	2 ND	Serializability
	3 RD	SECURITY AND INTEGRITY
	4 TH	Authorization and views
15 TH	1 ST	Authorization and views
	2 ND	Security constraints
	3 RD	Security constraints
	4 TH	Integrity Constraints
		Discuss Encryption

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Lesson plan for the Session Summer-2024
(6th SEMESTER CSE)

DISCIPLINE: Computer Science & Engineering	SEMESTER: 6TH	NAME OF THE TEACHING FACULTY: Mrs. Pratibha Patnaik
SUBJECT: CRYPTOGRAPHY & NETWORK SECURITY	NO. OF DAYS/PER WEEK CLASS ALLOTTED: 4	SEMESTER DURATION: 16/01/2024 to 26/4/2024 NO.OF WEEKS : 15
WEEK	CLASSDAY	THEORY/PRACTICAL TOPICS
1 ST	1 ST	Possible attacks on Computers Introduction
	2 ND	The need for security
	3 RD	Security approach
	4 TH	Principles of security
2 ND	1 ST	Types of attacks
	2 ND	Cryptography Concepts Introduction
	3 RD	Plain text & Cipher Text
	4 TH	Substitution techniques
3 RD	1 ST	Substitution techniques
	2 ND	Substitution techniques
	3 RD	Transposition techniques
	4 TH	Transposition techniques
4 TH	1 ST	Encryption & Decryption
	2 ND	Symmetric & Asymmetric key cryptography
	3 RD	Symmetric & Asymmetric key algorithms Introduction
	4 TH	Symmetric key algorithm types
5 TH	1 ST	Overview of Symmetric key cryptography
	2 ND	Data encryption standards
	3 RD	Data encryption standards
	4 TH	Data encryption standards
6 TH	1 ST	Over view of Asymmetric key cryptography
	2 ND	Over view of Asymmetric key cryptography
	3 RD	The RSA algorithm
		The RSA algorithm
	4 TH	The RSA algorithm
7 TH	1 ST	Symmetric & Asymmetric key cryptography
	2 ND	Digital signature
	3 RD	Digital signature
	4 TH	Digital certificate & Public key infrastructure Introduction

8TH	1ST	Digital certificates
	2ND	Digital certificates
	3RD	Private key management
	4TH	Private key management
9TH	1ST	Private key management
	2ND	PKIX Model
	3RD	PKIX Model
	4TH	Public key cryptography standards
10TH	1ST	Public key cryptography standards
	2ND	Internet security protocols
	3RD	Introduction
	4TH	Basic concept
11TH	1ST	Secure socket layer
	2ND	Secure socket layer
	3RD	Transport layer security
	4TH	Transport layer security
12TH	1ST	Secure Hyper text transfer protocol(SHTTP)
	2ND	Secure Hyper text transfer protocol(SHTTP)
	3RD	Time stamping protocol(TSP)
	4TH	Time stamping protocol(TSP)
13TH	1ST	Secure electronic transaction(SET)
	2ND	Secure electronic transaction(SET)
	3RD	User authentication
	4TH	Authentication basics
14TH	1ST	Password
	2ND	Authentication Tokens
	3RD	Certificate based authentication
	4TH	Biometric authentication
15TH	1ST	Network Security & VPN
	2ND	Brief introduction of TCP/IP
	3RD	Brief introduction of TCP/IP
	4TH	Firewall
	1ST	Firewall
	2ND	IP Security
	3RD	Virtual Private Network(VPN).
	4TH	Virtual Private Network(VPN).

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COURSE: Computer Science & Engineering	SEMESTER: 6TH	NAME OF THE TEACHING FACULTY: Mrs. Mousumi Subudhi
SUBJECT: INTERNET OF THINGS	NO. OF DAYS/PER WEEK CLASS ALLOTTED: 4	SEMESTER DURATION: 16/01/2024 to NO.OF WEEKS : 15
WEEK	CLASSDAY	THEORY/PRACTICAL TOPICS
1 ST	1 ST	Introduction to Internet of Things Introduction
	2 ND	Characteristics of IoT Applications of IoT
	3 RD	IoT Categories
	4 TH	IoT Enablers and connectivity layers
2 ND	1 ST	Baseline Technologies Sensor Actuator
	2 ND	IoT components and implementation Challenges for IoT
	3 RD	IoT Networking Introduction
	4 TH	Terminologies
3 RD	1 ST	Gateway Prefix allotment
	2 ND	Impact of mobility on Addressing
	3 RD	Multihoming
	4 TH	Deviation from regular Web
4 TH	1 ST	IoT identification and Data protocols
	2 ND	Connectivity Technologies
	3 RD	Introduction
	4 TH	IEEE 802.15.4
5 TH	1 ST	ZigBee, 6LoWPAN
	2 ND	RFID, HART and wireless HART
	3 RD	NFC, Bluetooth, Z wave, ISA100.11.A
	4 TH	Wireless Sensor Networks Introduction
6 TH	1 ST	Components of a sensor node Modes of Detection
	2 ND	Challenges in WSN Sensor Web
	3 RD	Cooperation and Behavior Nodes in WSN Self Management of WSN Social sensing WSN
	4 TH	Application of WSN Wireless Multimedia sensor network Wireless Nano sensor Networks
7 TH	1 ST	Underwater acoustic sensor networks WSN Coverage Stationary WSN, Mobile WSN

8 TH	2 ND	M2M communication
	3 RD	M2M communication
	4 TH	M2M Ecosystem
	1 ST	M2M service Platform
9 TH	2 ND	Interoperability
	3 RD	Programming with Arduino
	4 TH	Features of Arduino
	1 ST	Components of Arduino Board
	2 ND	Arduino IDE
10 TH	3 RD	Case Studies
	4 TH	Programming with Raspberry Pi
	1 ST	Introduction to Programming with Raspberry Pi
	2 ND	Architecture and Pin Configuration
	3 RD	Case studies
11 TH	4 TH	Implementation of IoT with Raspberry Pi
		Implementation of IoT with Raspberry PI
	1 ST	Software defined Networking
	2 ND	Limitation of current network
		Origin of SDN SDN Architecture
12 TH	4 TH	Rule Placement, Open flow Protocol Controller placement
	1 ST	Security in SDN Integrating SDN in IoT
	2 ND	Smart Home
	3 RD	Origin and example of Smart Home Technologies
	4 TH	Origin and example of Smart Home Technologies
13 TH	1 ST	Smart Home Implementation
	2 ND	Smart Home Implementation
	3 RD	Home Area Networks(HAN) Smart Home benefits and issues
	4 TH	Smart Cities Characteristics of Smart Cities
	1 ST	Smart city Frameworks
14 TH	2 ND	Challenges in Smart cities Data Fusion
	3 RD	Smart Parking
	4 TH	Energy Management in Smart cities

1ST	Industrial IoT
2ND	IIoT requirements
3RD	Design considerations
4TH	Applications of IIoT
	Benefits of IIoT
	Challenges of IIoT

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12/11/2023
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12/11/2024
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CSE Dept

DISCIPLINE: Computer Science & Engineering	SEMESTER: 6TH	Name Of The Teaching Faculty: Mr. Pramod Kumar Swain & Mrs. Pratibha Patnaik
SUBJECT: CLOUD COMPUTING	NO. OF DAYS/PER WEEK CLASS ALLOTTED: 4	SEMESTER DURATION: 16/01/2024 to 26/4/2024 NO.OF WEEKS : 15
WEEK	CLASSDAY	THEORY/PRACTICALTOPICS
1 st	1 ST	Introduction To Cloud Computing Historical development
	2 ND	Vision of Cloud Computing Characteristics of Cloud computing
	3 RD	Cloud computing Reference model Cloud computing environment
	4 TH	Cloud Service requirements Cloud and Dynamic Infrastructure
2 nd	1 ST	Cloud Adoption Cloud applications
	2 ND	Cloud Computing Architecture Introduction
	3 RD	Cloud Reference Model
	4 TH	Types of Clouds
3 rd	1 ST	Types of Clouds
	2 ND	Cloud Interoperability and standards
	3 RD	Cloud Interoperability and standards
	4 TH	Cloud computing Interoperability use cases
4 th	1 ST	Role of standards in Cloud Computing environment
	2 ND	Scalability and Fault Tolerance Introduction
	3 RD	Scalability and Fault Tolerance Cloud solutions
	4 TH	Cloud Ecosystem Cloud Business process management
5 th	1 ST	Portability and Interoperability Cloud Service management
	2 ND	Cloud Offerings
	3 RD	Testing under Control
	4 TH	Cloud service Controls
6 ^h	1 ST	Virtual desktop Infrastructure
	2 ND	Cloud Management and Virtualization Technology Introduction
	3 RD	Create a virtualized Architecture Data Centre
	4 TH	Resilience Agility
7 th	1 ST	Cisco Data Centre Network architecture Storage

	2ND	Provisioning
	3RD	Asset Management
	4TH	Concept of Map Reduce
	1ST	Cloud Governance
8th	2ND	Load Balancing
	3RD	High Availability
	4TH	Disaster Recovery
	1ST	Virtualisation
	2ND	Bet work Virtualisation
	3RD	Desktop and Application Virtualisation
	4TH	Desktop as a service
9th	1ST	Local desktop Virtualisation
	2ND	Virtualisation benefits
	3RD	Server Virtualisation
	4TH	Block and File level Storage Virtualisation
	1ST	Virtual Machine Monitor
	2ND	Infrastructure Requirements
	3RD	VLAN and VSAN
10th	4TH	Cloud Security
	1ST	Cloud Security Fundamentals
	2ND	Cloud security services
	3RD	Design Principles
	4TH	Design Principles
11th	1ST	Secure Cloud software requirements
	2ND	Secure Cloud software requirements
	3RD	Policy Implementation
	4TH	Cloud Computing Security Challenges
12th	1ST	Cloud Computing Security Architecture
	2ND	Architectural Considerations
	3RD	Information Classification
	4TH	Virtual Private Networks
	1ST	Public Key and Encryption Key management
13th	2ND	Digital certificates
	3RD	Key management
	4TH	Memory Cards
	1ST	Implementing Identity Management
	2ND	Controls and Autonomic System
	3RD	Market Based Management of Clouds
	4TH	Cloud Information security vendors
14th	1ST	Cloud Federation, characterization
	2ND	Cloud Federation stack
	3RD	Third Party Cloud service
	4TH	Case study
		Hadoop
		Introduction

1ST	Data Source
2ND	Data storage and Analysis
3RD	Data storage and Analysis
4TH	Comparison with other system


P. S. Maiti
16/1/24
P. S. Maiti
12/01/24
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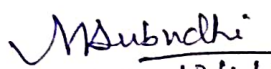
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16/1/2024
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DISCIPLINE: Computer Science & Engineering	SEMESTER: 6TH	NAME OF THE TEACHING FACULTY: Mrs. Yogesawari Magar
SUBJECT: ARTIFICIAL INTELLIGENCE & MACHINE LEARNING	NO. OF DAYS/PER WEEK CLASS ALLOTTED: 4	SEMESTER DURATION: 16/01/2024 to 26/4/2024 NO. OF WEEKS : 15
WEEK	CLASSDAY	THEORY/PRACTICAL TOPICS
1 st	1 ST	Introduction to AI Definition of AI,
	2 ND	History of AI Goals and Applications of AI
	3 RD	Intelligent agent Computer vision
	4 TH	Natural Language Processing
2 nd	1 ST	Turing test
	2 ND	Problem solving in Games
	3 RD	Problem solving in Games
	4 TH	Problem solving in Games
3 rd	1 ST	Problem solving in Games
	2 ND	Introduction to Search Algorithm Search, Search space
	3 RD	Search Tree Categories and Types of Search
	4 TH	Heuristic Algorithm vrs Solution Guaranteed Algorithm
4 th	1 ST	Heuristic Algorithm vrs Solution Guaranteed Algorithm
	2 ND	Local search and Optimal problem(Hill climbing, BFS,A*,AO*)
	3 RD	Local search and Optimal problem(Hill climbing, BFS,A*,AO*)
	4 TH	Local search and Optimal problem(Hill climbing, BFS,A*,AO*)
5 th	1 ST	Adversarial Search
	2 ND	AI and Game Playing
	3 RD	Knowledge Representation and Reasoning What to represent
	4 TH	Knowledge Properties of Knowledge Representation system
6 ^h	1 ST	Knowledge Properties of Knowledge Representation System
	2 ND	Approaches Knowledge Representation
	3 RD	Reasoning
	4 TH	Types of reasoning
7 th	1 ST	Types of reasoning
	2 ND	Machine Learning
	3 RD	Machine Learning
	4 TH	Machine Learning
8 th	1 ST	Statistical or Unsupervised Learning

	2ND	Statistical or Unsupervised Learning
	3RD	ML Properties
	4TH	ML Properties
9th	1ST	Reinforcement Learning
	2ND	Decision Tree
	3RD	Decision Tree
	4TH	Pattern Recognition
10th	1ST	Introduction to Pattern recognition
	2ND	Concept of Pattern recognition
	3RD	Design Principles of Pattern recognition system
	4TH	Design Principles of Pattern recognition system
11th	1ST	Statistical Pattern recognition System
	2ND	Statistical Pattern recognition System
	3RD	Machine Perception
	4TH	Machine Perception
12th	1ST	Line Finding and Interception
	2ND	Line Finding and Interception
	3RD	Object Identification
	4TH	Object Identification
13th	1ST	Expert System
	2ND	Introduction to Expert system
	3RD	Basic Architecture
	4TH	Basic Architecture
14th	1ST	Type of Problem Solved by Expert system
	2ND	Type of Problem Solved by Expert system
	3RD	Features of an Expert System
	4TH	Expert System Architectures
15th	1ST	Expert System Architectures
	2ND	Expert System Tools
	3RD	Existing Expert Systems
	4TH	Applications of Expert System Technology
		Applications of Expert System Technology


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