Lesson Plan for the Session Summer-2024
(4th SEMESTER CSE)

DISCIPLINE: **Computer Science & Engineering**	SEMESTER: **4TH**	NAME OF THE TEACHING FACULTY: **Mr. Pramod Kumar Swain**
SUBJECT: **Operating System**	NO. OF DAYS/PER WEEK CLASS ALLOTTED: **4**	SEMESTER DURATION: **16/01/2024** to 26/4/2024 NO.OF WEEKS : **15**
WEEK	**CLASSDAY**	**THEORY/PRACTICALTOPICS**
1ST	1ST	Objectives and Explain functions of operating system.
	2ND	Evolution of Operating system
	3RD	Structure of operating system
	4TH	PROCESSMANAGEMENT Process concept
2ND	1ST	process control, interacting processes
	2ND	Inter process messages.
	3RD	Implementation issues of Processes.
	4TH	Process scheduling
3RD	1ST	Job scheduling
	2ND	Process synchronization
	3RD	semaphore
	4TH	Principle of concurrency
4TH	1ST	types of scheduling
	2ND	MEMORY MANAGEMENT Memory allocation Techniques
	3RD	Contiguous memory allocation
	4TH	non contiguous memory allocation
5TH	1ST	Swapping
	2ND	Paging
	3RD	Segmentation,
	4TH	virtual memory using paging,
6TH	1ST	Demand paging
	2ND	page fault handling
	3RD	DEVICEMANAGEMENT
	4TH	Techniques for Device Management
7TH	1ST	Dedicated
	2ND	shared ,virtual
	3RD	Device allocation considerations I/O traffic control
	4TH	I/O Schedule
8TH	1ST	I/O Device handlers.
	2ND	SPOOLING
	3RD	DEADLOCKS

	4TH	Concept of deadlock
9TH	1ST	Types of deadlock
	2ND	Dead Lock Detection
	3RD	Resources allocation Graph
	4TH	Methods of Deadlock handling
10TH	1ST	Recovery &Prevention
	2ND	Explain Bankers Algorithm
	3RD	Safety Algorithm
	4TH	FILEMANAGEMENT
11TH	1ST	File organization
	2ND	Directory & file structure
	3RD	sharing of files
	4TH	File access methods
12TH	1ST	file systems
	2ND	reliability
	3RD	Allocation of disk space
	4TH	File protection
13TH	1ST	secondary storage management
	2ND	SYSTEMPROGRAMMING
	3RD	Concept of system programming
	4TH	and show difference from Application Complier
14TH	1ST	Compiler
	2ND	Concept of compiler
	3RD	functions of compiler
	4TH	functions of interpreter
15TH	1ST	Compare compiler and interpreter
	2ND	Seven phases of compiler
	3RD	Seven phases of compiler
	4TH	brief description of each phase

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DISCIPLINE: **Computer Science & Engineering**	SEMESTER: **4TH**	NAME OF THE TEACHING FACULTY: **Mrs. Yogeswari Magar**
SUBJECT: **Data Communication and Computer Network**	NO. OF DAYS/PER WEEK CLASS ALLOTTED: **4**	SEMESTER DURATION: **16/01/2024** to 26/4/2024 NO.OF WEEKS : **15**
WEEK	**CLASSDAY**	**THEORY/PRACTICALTOPICS**
1ST	1ST	Network
	2ND	Protocol
	3RD	Data Communication
	4TH	Architecture
2ND	1ST	Standards
	2ND	OSI
	3RD	TCP/IP
	4TH	Data Transmission &Media
3RD	1ST	Data transmission Concepts and Terminology
	2ND	Analog and Digital Data transmission
	3RD	Transmission impairments
	4TH	Channel capacity
4TH	1ST	Transmission media
	2ND	Guided Transmission
	3RD	Wireless Transmission
	4TH	Data Encoding
5TH	1ST	Concept of Data Encoding
	2ND	Digital data digital signals
	3RD	Digital data analog signals
	4TH	Analog data digital signals
6TH	1ST	Analog data analog signals
	2ND	Data Communication & Data link control
	3RD	Asynchronous and Synchronous Transmission
	4TH	Error Detection
7TH	1ST	Line configuration
	2ND	Flow Control
	3RD	Error Control
	4TH	Multiplexing
8TH	1ST	FDM synchronous TDM
	2ND	Statistical TDM
	3RD	Switching &Routing
	4TH	Circuit Switching networks
9TH	1ST	Packet Switching principles

	2ND	X.25
	3RD	Routing in Packet switching
	4TH	Congestion
10TH	1ST	Effects of congestion
	2ND	Congestion control
	3RD	Congestion control
	4TH	Traffic Management
11TH	1ST	Congestion Control in Packet Switching Network
	2ND	LAN Technology
	3RD	Topology
	4TH	Transmission Media
12TH	1ST	LAN protocol architecture
	2ND	Medium Access control
	3RD	Medium Access control
	4TH	Bridges, Hub, Switch
13TH	1ST	Ethernet (CSMA/CD),
	2ND	Ethernet (CSMA/CD),
	3RD	CSMA/CA
	4TH	Fiber Channel
14TH	1ST	Wireless LAN Technology
	2ND	TCP/IP
	3RD	TCP/IP Protocol Suite
	4TH	Basic Protocol functions
15TH	1ST	Principles of Inter networking
	2ND	Principles of Inter networking
	3RD	Internet Protocol operations
	4TH	Internet Protocol

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DISCIPLINE: **Computer Science & Engineering**	SEMESTER: **4TH**	NAME OF THE TEACHING FACULTY: **Mrs. Lipika Sandha**
SUBJECT: **MICROPROCESSOR & MICROCONTROLLER**	NO. OF DAYS/PER WEEK CLASS ALLOTTED: **5**	SEMESTER DURATION: **16/01/2024** to 26/4/2024 NO.OF WEEKS : **15**
WEEK	**CLASSDAY**	**THEORY/PRACTICALTOPICS**
1ST	1ST	Microprocessor
	2ND	Introduction to Microprocessor and Microcomputer & distinguish between them.
	3RD	Concept of Address bus, data bus, control bus & System Bus
	4TH	General Bus structure Block diagram.
2ND	1ST	Basic Architecture of 8085 (8 bit)Microprocessor
	2ND	Basic Architecture of 8085 (8 bit)Microprocessor
	3RD	Signal Description (Pin diagram) of 8085Microprocessor
	4TH	Signal Description (Pin diagram) of 8085Microprocessor
3RD	1ST	Register Organizations, Distinguish between SPR & GPR, Timing & Control Module,)
	2ND	Interrupts:-8085 Interrupts, Masking of Interrupt(SIM,RIM
	3RD	Interrupts:-8085 Interrupts, Masking of Interrupt(SIM,RIM
	4TH	Addressing data & Differentiate between one-byte, two-byte &three-byte instructions with examples
4TH	1ST	Addressing data & Differentiate between one-byte, two-byte &three-byte instructions with examples
	2ND	Addressing data & Differentiate between one-byte, two-byte &three-byte instructions with examples
	3RD	Addressing modes in instructions with suitable examples
	4TH	Addressing modes in instructions with suitable examples
5TH	1ST	Instruction Set of 8085(Data Transfer, Arithmetic, Logical, Branching, Stack& I/O , Machine Control)
	2ND	Instruction Set of 8085(Data Transfer, Arithmetic, Logical, Branching, Stack& I/O , Machine Control)
	3RD	Simple Assembly Language Programming of8085 Simple Addition &Subtraction
	4TH	Logic Operations (AND, OR, Complement 1's & 2's) & Masking of
6TH	1ST	Counters & Time delay (Single Register, Register Pair, More than Two Register)
	2ND	Looping, Counting & Indexing (Call/JMP etc).
	3RD	Stack &Subroutines programs, Code conversion, BCD Arithmetic & 16 Bit data Operation, Block Transfer.
7TH	1ST	Memory & I/O Addressing
	2ND	TIMINGDIAGRAMS

	3RD	Define op code, operand, T-State, Fetch cycle, Machine Cycle, Instruction cycle& discuss the concept of timing diagram.
	4TH	Draw timing diagram for memory read, memory write, I/O read, I/O write machine cycle.
8TH	1ST	Draw a neat sketch for the timing diagram for 8085 instruction(MOV,MVI,LDA instruction).
	2ND	Draw a neat sketch for the timing diagram for 8085 instruction(MOV,MVI,LDA instruction).
	3RD	Microprocessor Based System Development Aids Concept of interfacing
	4TH	Define Mapping &Data transfer mechanisms - Memory mapping & I/O Mapping Concept of Memory Interfacing:- Interfacing EPROM & RAM Memories
9TH	1ST	Concept of Address decoding for I/O devices Programmable Peripheral Interface:8255
	2ND	ADC & DAC with Interfacing
	3RD	Draw a neat sketch for the timing diagram for 8085 instruction(MOV,MVI,LDA instruction).
	4TH	Interfacing Seven Segment Displays
10TH	1ST	Generate square waves on all lines of8255 Design Interface a traffic light control system using8255.
	2ND	Microprocessor (Architecture and Programming-16 bit-8086)
	3RD	Register Organization of8086 Internal architecture of8086
	4TH	Signal Description of 8086 General Bus Operation& Physical Memory Organization
11TH	1ST	Minimum Mode& Timings, Maximum Mode& Timings
	2ND	Interrupts and Interrupt Service Routines, Interrupt Cycle,
	3RD	Non-Maskable Interrupt, Maskable Interrupt
	4TH	8086 Instruction Set & Programming: Addressing Modes, Instruction Set, Assembler Directives and Operators
12TH	1ST	8086 Instruction Set & Programming: Addressing Modes, Instruction Set, Assembler Directives and Operators
	2ND	Simple Assembly language programming using 8086instructions.
	3RD	Simple Assembly language programming using 8086instructions.
	4TH	Distinguish between Microprocessor &Microcontroller 8 bit & 16 bit microcontroller
13TH	1ST	CISC & RISC processor Architectureof8051Microcontroller
	2ND	Signal Descriptionof8051Microcontrollers Memory Organization-RAM structure, SFR
	4TH	Simple 8051 Assembly Language Programming Arithmetic& Logic Instructions , JUMP, LOOP, CALL

		Instructions, I/O Port Programming
	2ND	Simple 8051 Assembly Language Programming Arithmetic& Logic Instructions , JUMP, LOOP, CALL Instructions, I/O Port Programming
	3RD	Interrupts
	4TH	Timer &Counters
15TH	1ST	Timer &Counters
	2ND	Serial Communication
	3RD	Microcontroller Interrupts and Interfacing to8255
	4TH	Microcontroller Interrupts and Interfacing to8255

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Lopika Saha
Lect. (Electronics)

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DISCIPLINE: Computer Science & Engineering	SEMESTER: 4TH	NAME OF THE TEACHING FACULTY: Mrs. MOUSUMI SUBUDHI
SUBJECT: DATABASE MANAGEMENT SYSTEM	NO. OF DAYS/PER WEEK CLASS ALLOTTED: 4	SEMESTER DURATION: 16/01/2024 to 26/4/2024 NO.OF WEEKS : 15
WEEK	CLASSDAY	THEORY/PRACTICALTOPICS
1ST	1ST	BASIC CONCPETS OFDBMS
	2ND	Purpose of database Systems
	3RD	Explain Data abstraction
	4TH	Data base users
2ND	1ST	Data definition language
	2ND	Data Dictionary
	3RD	Data independence
	4TH	Entity relationship models
3RD	1ST	Entity sets and Relationship sets
	2ND	Explain Attributes Mapping constraints
	3RD	E-R Diagram
	4TH	E-R Diagram
4TH	1ST	Relational model
	2ND	Hierarchical model
	3RD	Network model
	4TH	RELATIONALDATABASE Introduction
5TH	1ST	Relation alalgebra
	2ND	Different operators select, project, join , simple Examples
	3RD	Different operators select, project, join , simple Examples
	4TH	Different operators select, project, join , simple Examples
6TH	1ST	NORMALIZATION IN RELATIONALSYSTEM Introduction
	2ND	Functional Dependencies
	3RD	Functional Dependencies
	4TH	Lossless join
7TH	1ST	Importance of normalization
	2ND	Compare First second and third normal forms
	3RD	Compare First second and third normal forms
	4TH	Compare First second and third normal forms
8TH	1ST	Explain BCNF
	2ND	Explain BCNF
		STRUCTURED QUERYLANGUAGE Introduction

	3RD	Elementary idea of Query language
	4TH	Queries in SQL
9TH	1ST	Queries in SQL
	2ND	Simple queries to create, update, insert in SQL
	3RD	Simple queries to create, update, insert in SQL
	4TH	Simple queries to create, update, insert in SQL
	2ND	Simple queries to create, update, insert in SQL
	3RD	TRANSACTION PROCESSINGCONCEPTS Introduction
	4TH	Idea about transaction processing
11TH	1ST	Transaction & system concept
	2ND	Transaction & system concept
	3RD	Desirable properties of transaction
	4TH	Desirable properties of transaction
12TH	1ST	Schedules and recoverability
	2ND	Schedules and recoverability
	3RD	CONCURRENCY CONTROLCONCEPTS
	4TH	Basic concepts
13TH	1ST	Locks, Live Lock, Dead Lock,
	2ND	Locks, Live Lock, Dead Lock,
	3RD	Locks, Live Lock, Dead Lock,
	4TH	Locks, Live Lock, Dead Lock,
14TH	1ST	Serializability
	2ND	SECURITY ANDINTEGRITY
	3RD	Authorization and views
	4TH	Authorization and views
15TH	1ST	Security constraints
	2ND	Security constraints
	3RD	Integrity Constraints
	4TH	Discuss Encryption

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Lesson plan for the Session Summer-2024
(6th SEMESTER CSE)

DISCIPLINE: **Computer Science & Engineering**	SEMESTER: **6TH**	NAME OF THE TEACHING FACULTY: **Mrs. Pratibha Patnaik**
SUBJECT: **CRYPTOGRAPHY & NETWORK SECURITY**	NO. OF DAYS/PER WEEK CLASS ALLOTTED: **4**	SEMESTER DURATION: **16/01/2024** to 26/4/2024 NO.OF WEEKS : **15**
WEEK	**CLASSDAY**	**THEORY/PRACTICALTOPICS**
1ST	1ST	**Possible attacks on Computers** Introduction
	2ND	The need for security
	3RD	Security approach
	4TH	Principles of security
2ND	1ST	Types of attacks
	2ND	Cryptography Concepts Introduction
	3RD	Plain text & Cipher Text
	4TH	Substitution techniques
3RD	1ST	Substitution techniques
	2ND	Substitution techniques
	3RD	Transposition techniques
	4TH	Transposition techniques
4TH	1ST	Encryption &Decryption
	2ND	Symmetric & Asymmetric key cryptography
	3RD	Symmetric & Asymmetric key algorithms Introduction
	4TH	Symmetric key algorithm types
5TH	1ST	Overview of Symmetric key cryptography
	2ND	Data encryption standards
	3RD	Data encryption standards
	4TH	Data encryption standards
6TH	1ST	Over view of Asymmetric key cryptography
	2ND	Over view of Asymmetric key cryptography
	3RD	The RSA algorithm
		The RSA algorithm
	4TH	The RSA algorithm
7TH	1ST	Symmetric & Asymmetric key cryptography
	2ND	Digital signature
	3RD	Digital signature
	4TH	Digital certificate & Public key infrastructure Introduction

8TH	1ST	Digital certificates
	2ND	Digital certificates
	3RD	Private key management
	4TH	Private key management
9TH	1ST	Private key management
	2ND	PKIX Model
	3RD	PKIX Model
	4TH	Public key cryptography standards
10TH	1ST	Public key cryptography standards
	2ND	Internet security protocols Introduction
	3RD	Basic concept
	4TH	Secure socket layer
11TH	1ST	Secure socket layer
	2ND	Transport layer security
	3RD	Transport layer security
		Secure Hyper text transfer protocol(SHTTP)
	4TH	Secure Hyper text transfer protocol(SHTTP)
12TH	1ST	Time stamping protocol(TSP)
	2ND	Time stamping protocol(TSP)
	3RD	Secure electronic transaction(SET)
	4TH	Secure electronic transaction(SET)
13TH	1ST	User authentication Authentication basics
	2ND	Password
	3RD	Authentication Tokens
	4TH	Certificate based authentication
14TH	1ST	Biometric authentication
	2ND	Network Security &VPN Brief introduction of TCP/IP
	3RD	Brief introduction of TCP/IP
	4TH	Firewall
15TH	1ST	Firewall
	2ND	IP Security
	3RD	Virtual Private Network(VPN).
	4TH	Virtual Private Network(VPN).

Ppatnaik
16/1/24
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LINE: Computer nce & Engineering	SEMESTER: 6TH	NAME OF THE TEACHING FACULTY: Mrs. Mousumi Subudhi
BJECT: INTERNET OF THINGS	NO. OF DAYS/PER WEEK CLASS ALLOTTED: 4	SEMESTER DURATION: 16/01/2024 to NO.OF WEEKS : 15
WEEK	CLASSDAY	THEORY/PRACTICALTOPICS
1ST	1ST	Introduction to Internet of Things Introduction
	2ND	Characteristics of IoT Applications of IoT
	3RD	IoT Categories
	4TH	IoT Enablers and connectivity layers
2ND	1ST	Baseline Technologies Sensor Actuator
	2ND	IoT components and implementation Challenges for IoT
	3RD	IOT Networking Introduction
	4TH	Terminologies
3RD	1ST	Gateway Prefix allotment
	2ND	Impact of mobility on Addressing
	3RD	Multihoming
	4TH	Deviation from regular Web
4TH	1ST	IoT identification and Data protocols
	2ND	Connectivity Technologies
	3RD	Introduction
	4TH	IEEE 802.15.4
5TH	1ST	ZigBee,6LoWPAN
	2ND	RFID, HART and wireless HART
	3RD	NFC, Bluetooth, Z wave,ISA100.11.A
	4TH	Wireless Sensor Networks Introduction
6TH	1ST	Components of a sensor node Modes of Detection
	2ND	Challenges in WSN Sensor Web
	3RD	Cooperation and Behavior Nodes in WSN Self Management of WSN Social sensing WSN
	4TH	Application of WSN Wireless Multimedia sensor network Wireless Nano sensor Networks
7TH	1ST	Underwater acoustic sensor networks WSN Coverage Stationary WSN, Mobile WSN

	2ND	M2M communication
	3RD	M2M communication
	4TH	M2M Ecosystem
8TH	1ST	M2M service Platform
	2ND	Interoperability
	3RD	Programming with Arduino
	4TH	Features of Arduino
9TH	1ST	Components of Arduino Board
	2ND	Arduino IDE
	3RD	Case Studies
	4TH	Programming with Raspberry Pi
10TH	1ST	Introduction to Programming with Raspberry Pi
	2ND	Architecture and Pin Configuration
	3RD	Case studies
	4TH	Implementation of IoT with Raspberry Pi
		Implementation of IoT with Raspberry PI
11TH	1ST	Software defined Networking
	2ND	Limitation of current network
		Origin of SDN SDN Architecture
	4TH	Rule Placement, Open flow Protocol Controller placement
12TH	1ST	Security in SDN Integrating SDN in IoT
	2ND	Smart Home
	3RD	Origin and example of Smart Home Technologies
	4TH	Origin and example of Smart Home Technologies
13TH	1ST	Smart Home Implementation
	2ND	Smart Home Implementation
	3RD	Home Area Networks(HAN) Smart Home benefits and issues
	4TH	Smart Cities Characteristics of Smart Cities
14TH	1ST	Smart city Frameworks
	2ND	Challenges in Smart cities Data Fusion
	3RD	Smart Parking
	4TH	Energy Management in Smart cities

	1ST	Industrial IoT IIoT requirements
	2ND	Design considerations
	3RD	Applications of IIoT
	4TH	Benefits of IIoT Challenges of IIoT

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DISCIPLINE: **Computer Science & Engineering**	SEMESTER: **6TH**	Name Of The Teaching Faculty: **Mr. Pramod Kumar Swain & Mrs. Pratibha Patnaik**
SUBJECT: **CLOUD COMPUTING**	NO. OF DAYS/PER WEEK CLASS ALLOTTED: **4**	SEMESTER DURATION: **16/01/2024** to 26/4/2024 NO.OF WEEKS : **15**
WEEK	**CLASSDAY**	**THEORY/PRACTICALTOPICS**
1st	1ST	**Introduction To Cloud Computing** Historical development
	2ND	Vision of Cloud Computing Characteristics of Cloud computing
	3RD	Cloud computing Reference model Cloud computing environment
	4TH	Cloud Service requirements Cloud and Dynamic Infrastructure
2nd	1ST	Cloud Adoption Cloud applications
	2ND	Cloud Computing Architecture Introduction
	3RD	Cloud Reference Model
	4TH	Types of Clouds
3rd	1ST	Types of Clouds
	2ND	Cloud Interoperability and standards
	3RD	Cloud Interoperability and standards
	4TH	Cloud computing Interoperability use cases
4th	1ST	Role of standards in Cloud Computing environment
	2ND	Scalability and Fault Tolerance Introduction
	3RD	Scalability and Fault Tolerance Cloud solutions
	4TH	Cloud Ecosystem Cloud Business process management
5th	1ST	Portability and Interoperability Cloud Service management
	2ND	Cloud Offerings
	3RD	Testing under Control
	4TH	Cloud service Controls
6h	1ST	Virtual desktop Infrastructure
	2ND	Cloud Management and Virtualization Technology Introduction
	3RD	Create a virtualized Architecture Data Centre
	4TH	Resilience Agility
7th	1ST	Cisco Data Centre Network architecture Storage

	2ND	Provisioning Asset Management
	3RD	Concept of Map Reduce Cloud Goverance
	4TH	Load Balancing High Availability
8th	1ST	Disaster Recovery Virtualisation
	2ND	Bet work Virtualisation Desktop and Application Virtualisation
	3RD	Desktop as a service Local desktop Virtualisation
	4TH	Virtualisation benefits Server Virtualisation
9th	1ST	Block and File level Storage Virtualisation
	2ND	Virtual Machine Monitor
	3RD	Infrastructure Requirements
	4TH	VLAN and VSAN
10th	1ST	Cloud Security Cloud Security Fundamentals
	2ND	Cloud security services
	3RD	Design Principles
	4TH	Design Principles
11th	1ST	Secure Cloud software requirements
	2ND	Secure Cloud software requirements
	3RD	Policy Implementation
	4TH	Cloud Computing Security Challenges
12th	1ST	Cloud Computing Security Architecture Architectural Considerations
	2ND	Information Classification Virtual Private Networks
	3RD	Public Key and Encryption Key management Digital certificates
	4TH	Key management Memory Cards
13th	1ST	Implementing Identity Management Controls and Autonomic System
	2ND	Market Based Management of Clouds
	3RD	Cloud Information security vendors
	4TH	Cloud Federation, charactrization
14th	1ST	Cloud Federation stack
	2ND	Third Party Cloud service Case study
	3RD	Hadoop Introduction
	4TH	

	1ST	Data Source
	2ND	Data storage and Analysis
	3RD	Data storage and Analysis
	4TH	Comparison with other system

Prasad 16/1/24

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DISCIPLINE: Computer Science & Engineering	SEMESTER: 6TH	NAME OF THE TEACHING FACULTY: Mrs. Yogesawari Magar
SUBJECT: ARTIFICIAL INTELIGENCE & MACHINE LEARNING	NO. OF DAYS/PER WEEK CLASS ALLOTTED: 4	SEMESTER DURATION: 16/01/2024 to 26/4/2024 NO.OF WEEKS : 15
WEEK	CLASSDAY	THEORY/PRACTICALTOPICS
1st	1ST	Introduction to AI Definition of AI,
	2ND	History of AI Goals and Applications of AI
	3RD	Intelligent agent Computer vision
	4TH	Natural Language Processing
2nd	1ST	Turing test
	2ND	Problem solving in Games
	3RD	Problem solving in Games
	4TH	Problem solving in Games
3rd	1ST	Problem solving in Games
	2ND	Introduction to Search Algorithm Search, Search space
	3RD	Search Tree Categories and Types of Search
	4TH	Heuristic Algorithm vrs Solution Guaranteed Algorithm
4th	1ST	Heuristic Algorithm vrs Solution Guaranteed Algorithm
	2ND	Local search and Optimal problem(Hill climbing, BFS,A*,AO*)
	3RD	Local search and Optimal problem(Hill climbing, BFS,A*,AO*)
	4TH	Local search and Optimal problem(Hill climbing, BFS,A*,AO*)
5th	1ST	Adversarial Search
	2ND	AI and Game Playing
	3RD	Knowledge Representation and Reasoning What to represent
	4TH	Knowledge Properties of Knowledge Representation system
6h	1ST	Knowledge Properties of Knowledge Representation System
	2ND	Approaches Knowledge Representation
	3RD	Reasoning
	4TH	Types of reasoning
7th	1ST	Types of reasoning
	2ND	Machine Learning
	3RD	Machine Learning
	4TH	Machine Learning
8th	1ST	Statistical or Unsupervised Learning

	2ND	Statistical or Unsupervised Learning
	3RD	ML Properties
	4TH	ML Properties
9th	1ST	Reinforcement Learning
	2ND	Decision Tree
	3RD	Decision Tree
	4TH	Pattern Recognition Introduction to Pattern recognition
10th	1ST	Concept of Pattern recognition
	2ND	Design Principles of Pattern recognition system
	3RD	Design Principles of Pattern recognition system
	4TH	Statistical Pattern recognition System
11th	1ST	Statistical Pattern recognition System
	2ND	Machine Perception
	3RD	Machine Perception
	4TH	Line Finding and Interception
12th	1ST	Line Finding and Interception
	2ND	Object Identification
	3RD	Object Identification
	4TH	Expert System
13th	1ST	Introduction to Expert system
	2ND	Basic Architecture
	3RD	Basic Architecture
	4TH	Type of Problem Solved by Expert system
14th	1ST	Type of Problem Solved by Expert system
	2ND	Features of an Expert System
	3RD	Expert System Architectures
	4TH	Expert System Architectures
15th	1ST	Expert System Tools
	2ND	Existing Expert Systems
	3RD	Applications of Expert System Technology
	4TH	Applications of Expert System Technology

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